

OSMM&N File No. 273853US40CIP
Serial No. 10/814,011

Dept.: E/M
By: RDK/YO/mda

In the matter of the Application of: Toshi K. UCHIDA, et al.
For: EMBEDDED OPTICAL COUPLING IN CIRCUIT BOARDS

Due Date: N/A

The following has been received in the U.S. Patent Office on the date stamped hereon:

- Dep. Acct. Order Form
- Cover Letter
- Comments on Statement of Reasons for Allowance





Docket No.: 273853US40CIP

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COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/814,011

Applicants: Toshi K. UCHIDA, et al.

Filing Date: March 31, 2004

Allowed: July 31, 2006

For: EMBEDDED OPTICAL COUPLING IN CIRCUIT
BOARDS

Group Art Unit: 2874

Examiner: PEACE, RHONDA S

SIR:

Attached hereto for filing are the following papers:

Comments on Statement of Reasons for Allowance

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

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IN RE APPLICATION OF

TOSHI K. UCHIDA, ET AL.

SERIAL NO: 10/814,011

FILED: MARCH 31, 2004

FOR: EMBEDDED OPTICAL COUPLING
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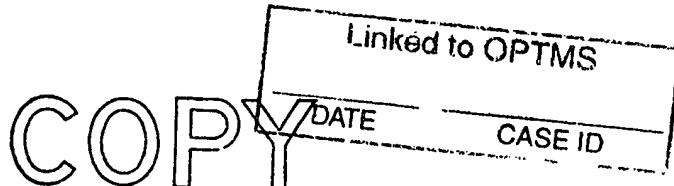
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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCECOMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Notice of Allowability dated July 31, 2006, Applicants respectfully submit comments on the statement of reasons for allowance regarding the above-identified application.

The statement of reasons for allowance of Claim 37 provided at page 3 of the Notice of Allowability indicates that “[t]he most relevant prior does not disclose, nor does it reasonably suggest an optoelectronic module . . . , where the lenses and the reflector form a unitary optical element *with a biconvex top surface and an internally reflecting bottom surface*” (emphasis original). However, it is respectfully submitted that Claim 37 recites “said one or more optical lenses and said optical reflector being formed as different surfaces of a unitary optical element of light transmitting material; and said unitary optical element having *a lenticular top surface and an internally reflecting bottom surface*” (emphasis added in italic)



Also, the statement of reasons for allowance of Claim 114 indicates that “[t]he relevant prior art does not disclose, nor does it reasonably suggest an optoelectronic module comprising a unitary optical element with a *biconvex* top surface and an internally reflecting bottom surface” (emphasis original). Applicants, however, respectfully submit that Claim 114 recites “[a]n optoelectronic module comprising: … an optical reflector formed as a surface of a unitary optical element of light transmitting material …; and one or more optical lenses formed as another surface of said unitary optical element of light transmitting material having *a lenticular top surface* and an internally reflecting bottom surface …” (emphasis added in italic).

Respectfully submitted,

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(OSMMN 06/04)

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